

PCI Express® 6.0 x16 CEM Interposer with CrossSync® PHY

Key Features

- Quick and simple set-up
- Allows capturing and decoding of all PCIe traffic between host and device
- TAP 6 technology for non-intrusive Probing
- Supports SMBus
- Supports bifurcated lane testing
- Includes DUT support mechanism
- G6x16 CEM Interposer CrossSync™ PHY capable
- Gen6x16 Interposer supports: PWRBRK#,
 - PERST#, WAKE#, CLKREQ# sideband signals
- Supports CXL Protocol
- Supports NVMe Protocol

Specifications

Dimensions	169.6 x 173.3mm (6.68" x 6.82")
Link Width	Up to x16
Data Rates	2.5 GT/s, 5.0 GT/s, 8.0 GT/s, 16.0 GT/s, 32.0 GT/s, 64.0 GT/s
Environmental	Temperature (operating): 5° to 40°C (41° to 104°F)
	Temperature (non-operating): -20° to 60°C (-4° to 140° F)
	Humidity (operating): 5% to 80% RH (non-condensing) at ≤30°C, 50% max RH (non-condensing) at 40°C
	Humidity (non-operating): 5% to 95% max RH (non-condensing)

The Teledyne LeCroy PCI Express CEM Interposer provides a simple and easy-to-use way to probe traffic up to 64GT/s for PCIe® 6.0 based products between a host and PCIe add-in card with the next generation PCIe protocol analyzer.

Connecting this interposer to a Teledyne LeCroy Summit PCIe analyzer allows decoding and display of data flowing in both directions and across all lanes, and will display data traffic using the industry-standard CATC Trace™ data display, along with a wide range of traffic and error reports.

The PCIe 6.0 CEM Interposer supports data rates of 2.5, 5.0, 8.0, 16.0, 32.0 and 64 GT/s and support link widths of up to x16. Advanced TAP6 technology simplifies the signal acquisition for protocol analysis and simultaneously maintains the transmitters signal integrity transparently.

The PCIe 6.0 CEM Interposer is compatible with Summit T48, T416, M5x, T54, T516, and M616 protocol analyzers. Interposers will support a complete suite of testing capability to analyze PCIe communication including SMBus (out of band signaling) and allow recording and analysis of low power modes supported through CLKREQ# and SRIS.

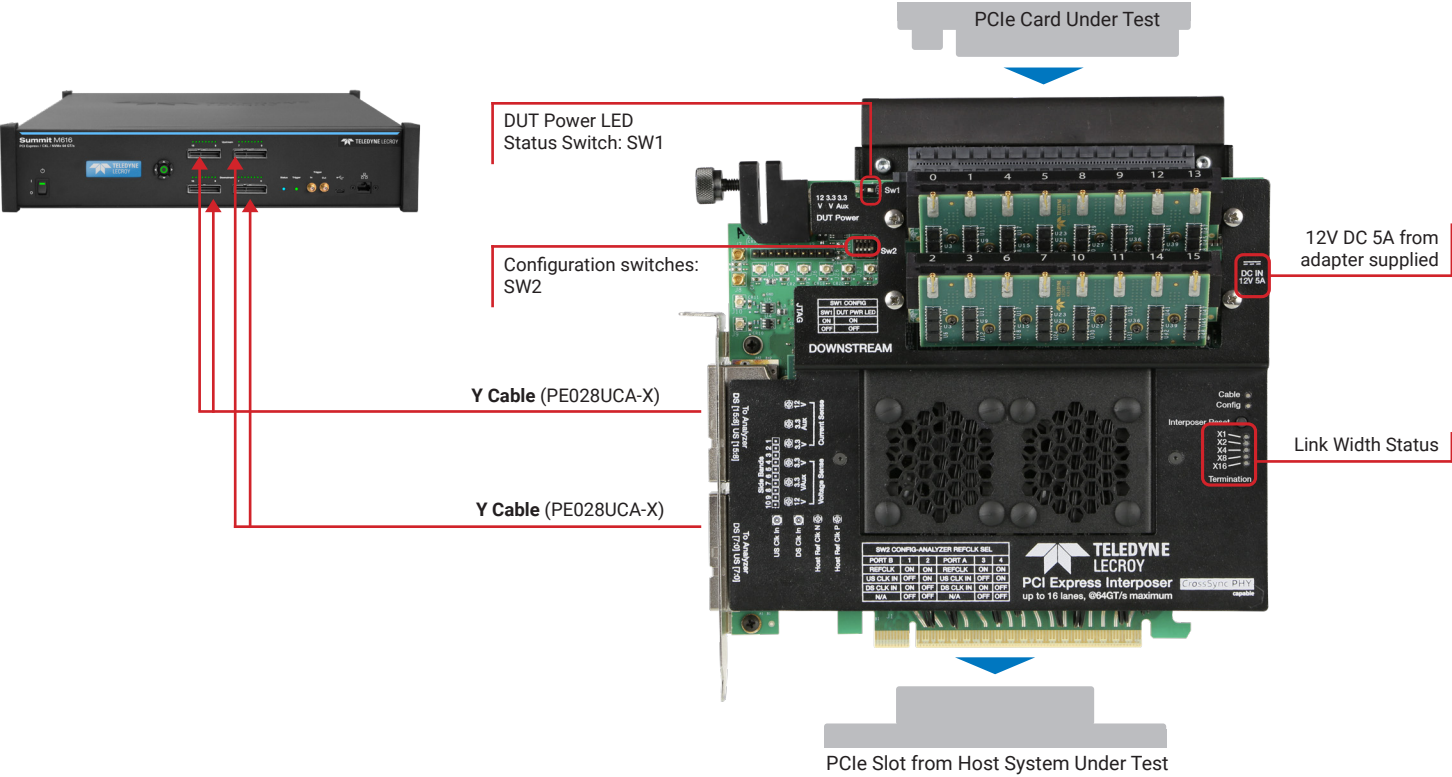
The PCIe 6.0 CEM Interposer with CrossSync PHY support is a powerful and

versatile tool for all developers working up to 64GT/s speed in their development projects.

When the PCIe 6.0x16 CEM interposer is upgraded with CrossSync PHY technology it will provide enhanced power management and link training equalization debugging capability, enabling users to see and correlate both the physical and protocol layers in a unified time aligned view. No other solution gives this type of insight into link behavior.



Note: Diagram depicts x16 interposer used with a Summit M616 Protocol Analyzer. For more configuration information, see Quick Start Guide.



PCIe 6.0 EDSFF/OCF Interposer Analyzer and Cable Compatibility		
	Analyzer Platform	Cable Requirement(s)
	Summit T48 Protocol Analyzer (Standard)	PE031UCA-X (Y-Cable)
	Summit T48 Protocol Analyzer (Multiport)	PE030UCA-X (Straight Cable)
	Summit T416 Protocol Analyzer (Standard)	PE031UCA-X (Y-Cable)
	Summit T416 Protocol Analyzer (Multiport)	PE030UCA-X (Straight Cable)
	Summit T54 Protocol Analyzer (G5 PHY)	PE027UCA-X (Straight Cable)
	Summit M5x Protocol Analyzer	PE029UCA-X (Y-Cable)
	Summit T516 Protocol Analyzer (Standard)	PE028UCA-X (Y-Cable)
	Summit T516 Protocol Analyzer (Multiport)	PE027UCA-X (Straight Cable)
	Summit M616 Protocol Analyzer (Standard)	PE028UCA-X (Y-Cable)
	Summit M616 Protocol Analyzer (Multiport)	PE027UCA-X (Straight Cable)

Ordering Information

Product Description

PCIe 6.0 x16 Interposer
CrossSync PHY Upgrade

Product Code

PE6117UIA-X
PE6817ACA-X



Local sales offices are located throughout the world.
Visit our website to find the most convenient location.
1-800-5-LeCroy • teledynelecroy.com



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